

Fig.1

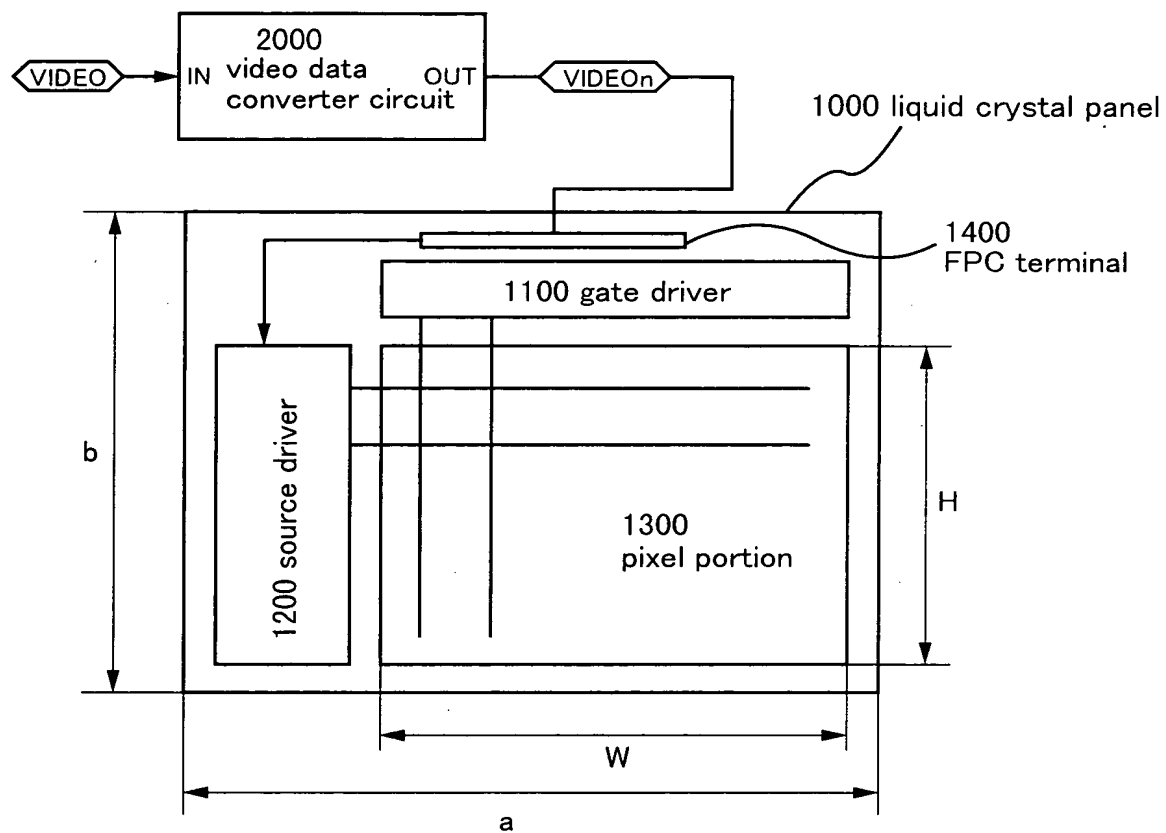


FIG. 2 is a schematic diagram of a video display device in accordance with the present invention. The device includes a video input terminal 100, a video source driver 120, a gate driver 110, and a display panel 130. The video input terminal 100 is connected to the video source driver 120. The video source driver 120 is connected to the gate driver 110. The gate driver 110 is connected to the display panel 130. The display panel 130 includes a plurality of pixels 1310, 1320, 1330, and 1340. Each pixel includes a first transistor 1310, a second transistor 1320, a third transistor 1330, and a fourth transistor 1340. The first transistor 1310 is connected to the video source driver 120. The second transistor 1320 is connected to the gate driver 110. The third transistor 1330 is connected to the video source driver 120. The fourth transistor 1340 is connected to the gate driver 110. The display panel 130 is connected to a common terminal COM.

Fig.2

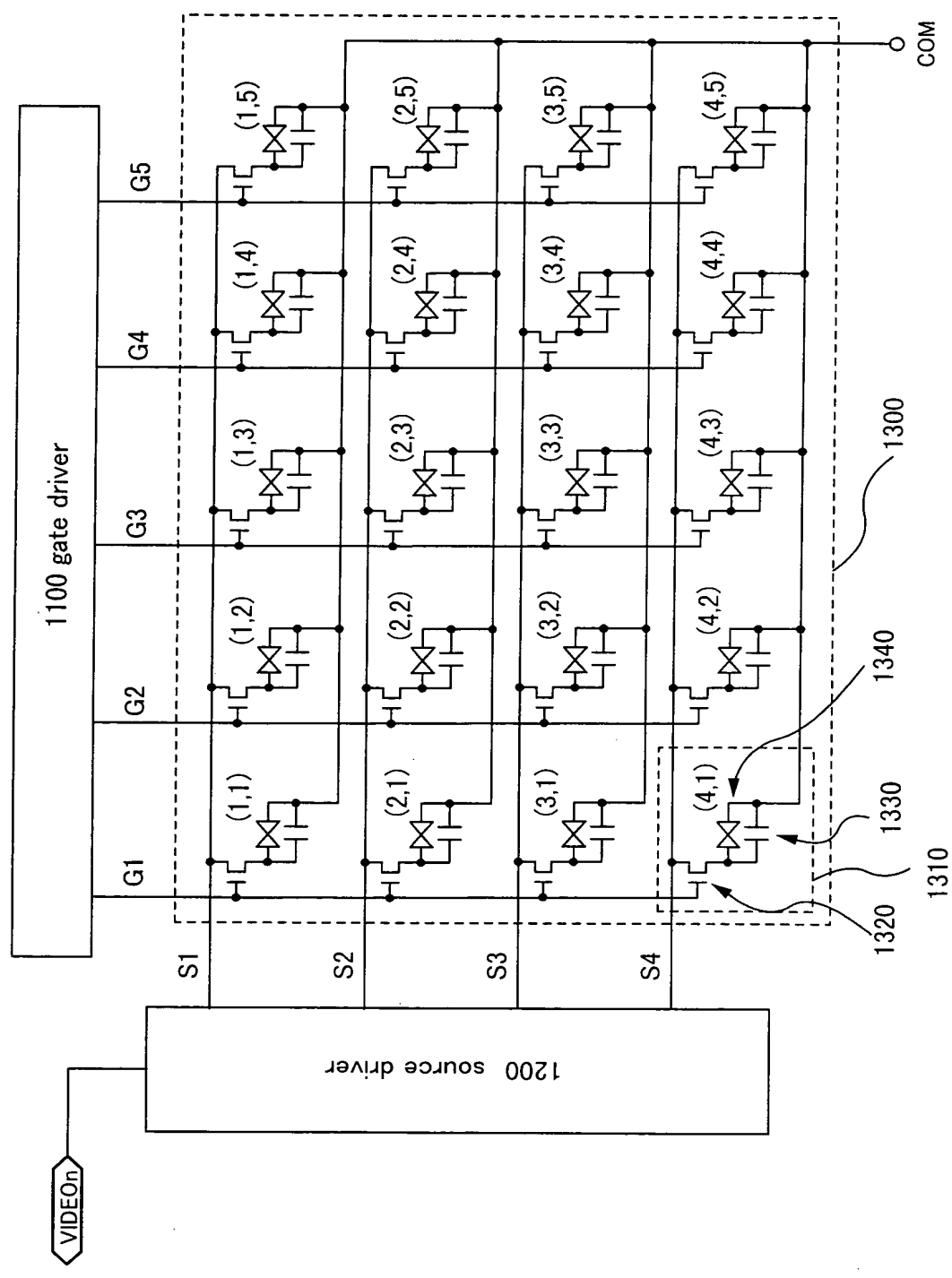




Fig. 4

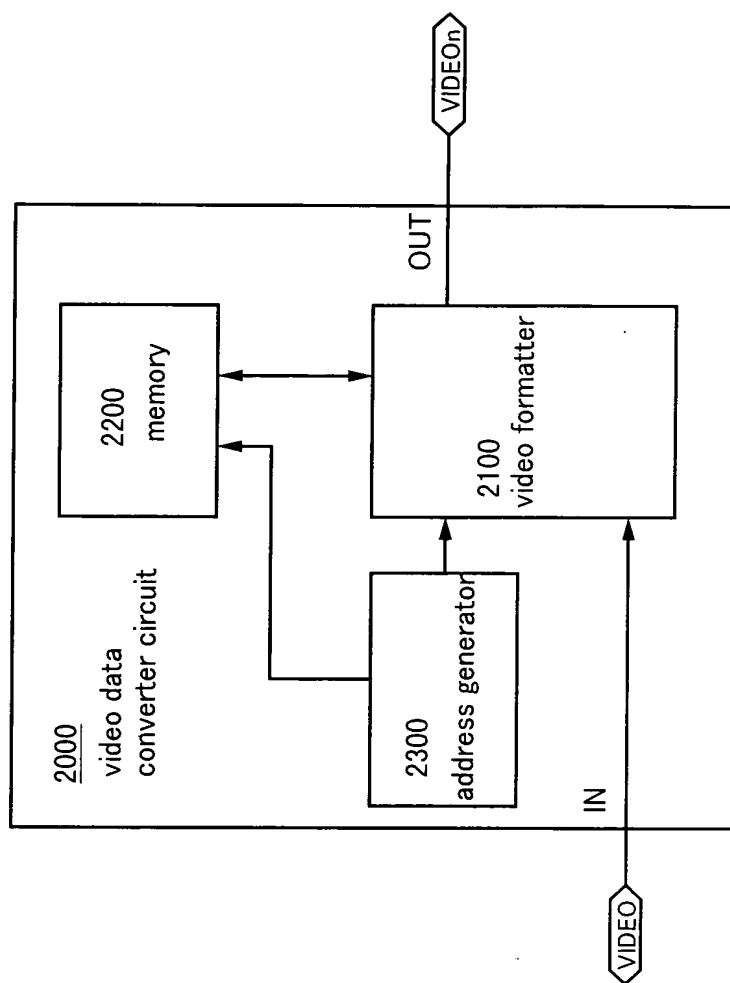
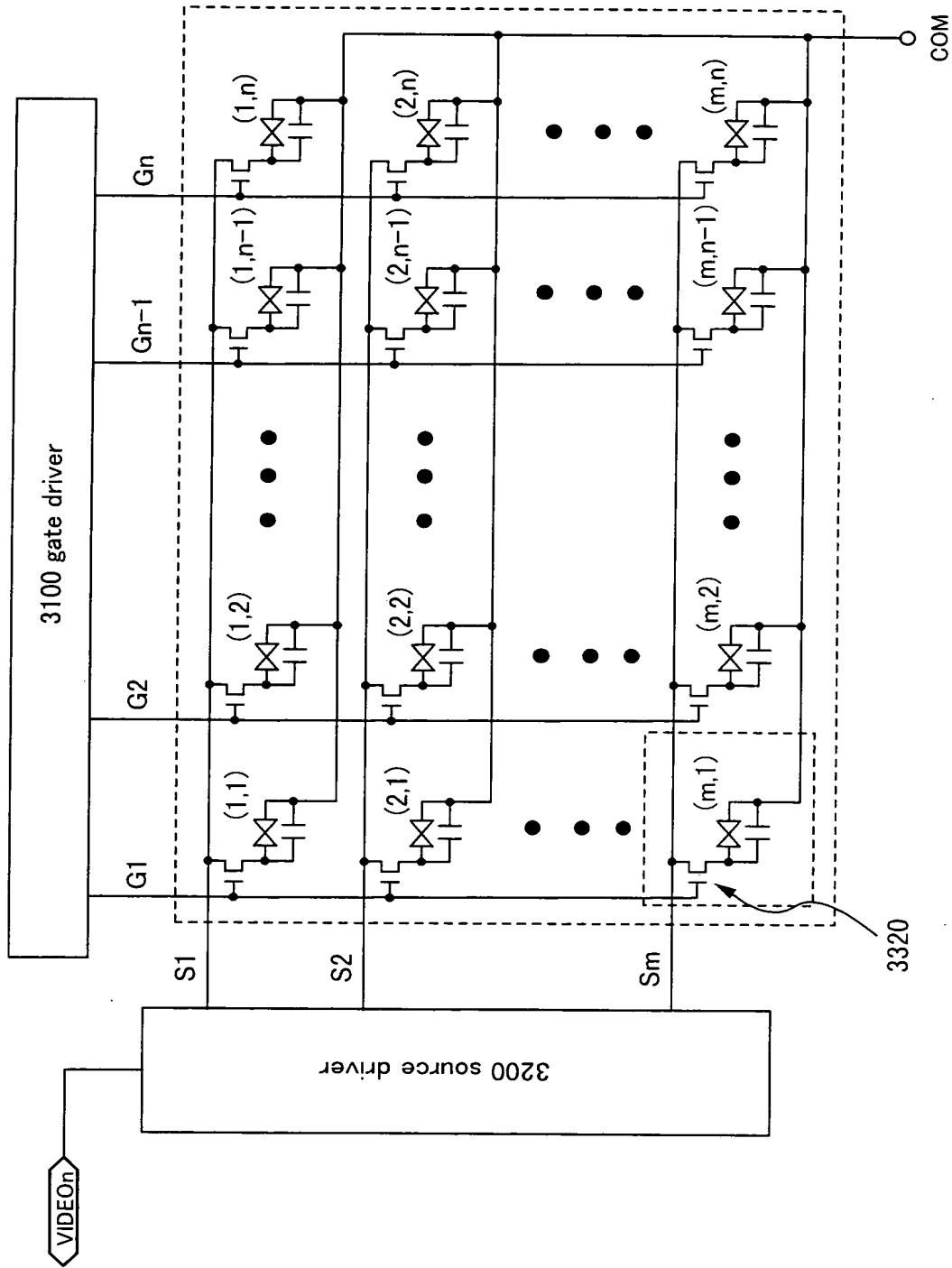


Fig.5



video data n (VIDEO)

Fig.6

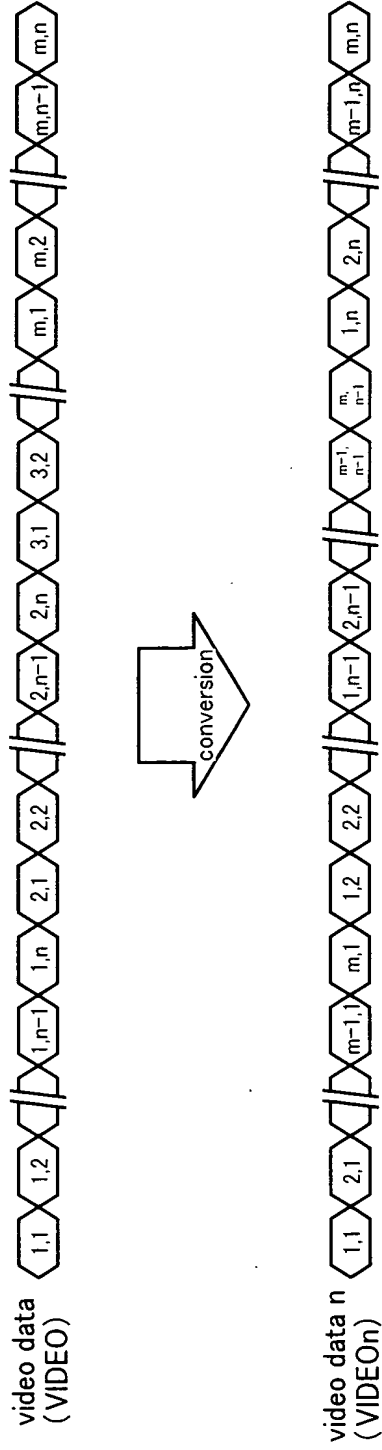


Fig. 7

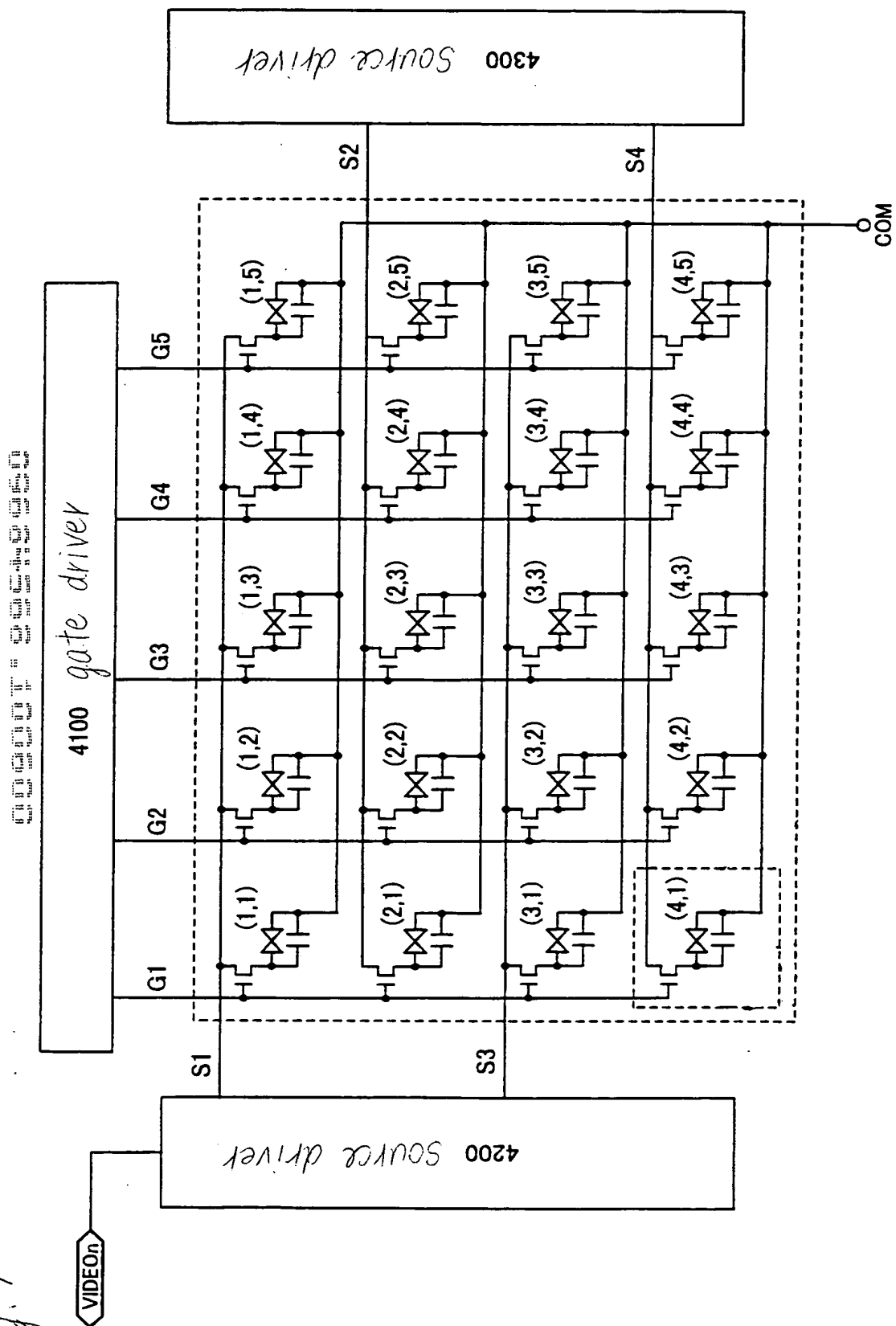


Fig.8A

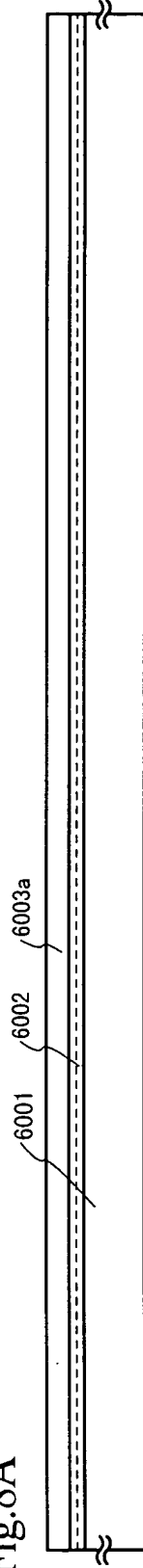


Fig.8B

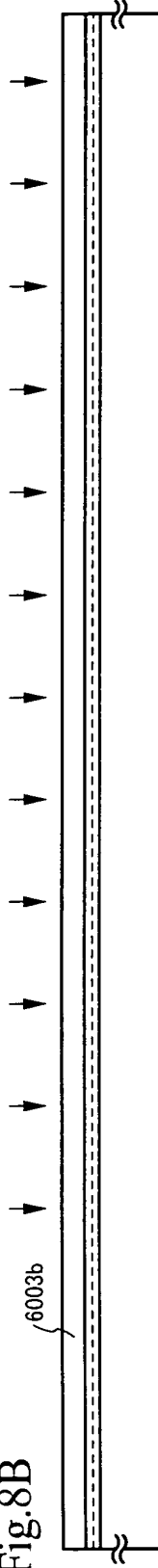


Fig.8C

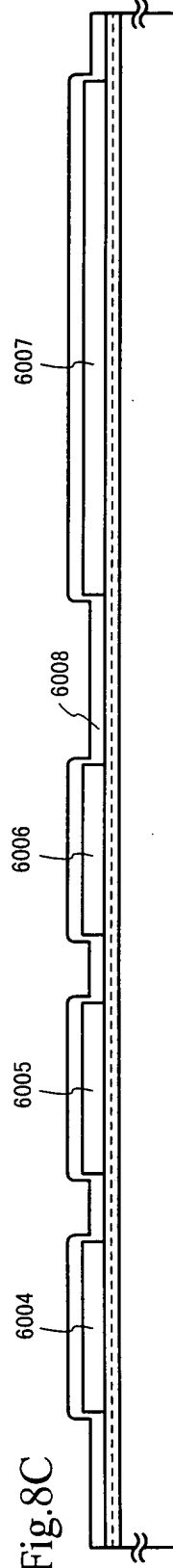
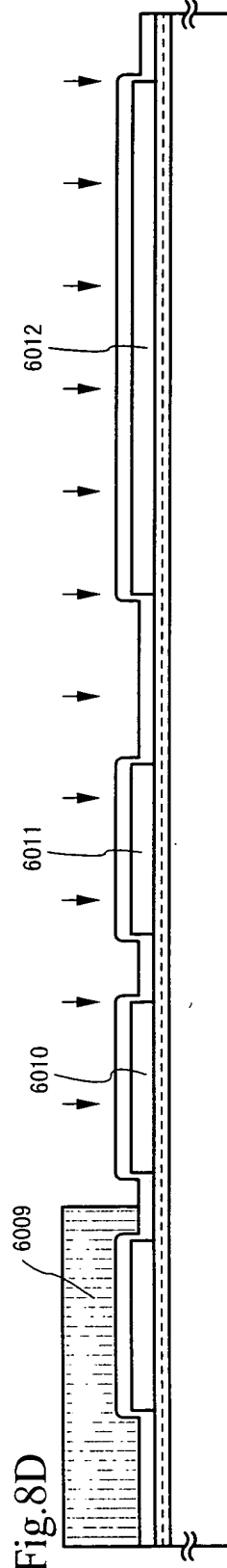


Fig.8D





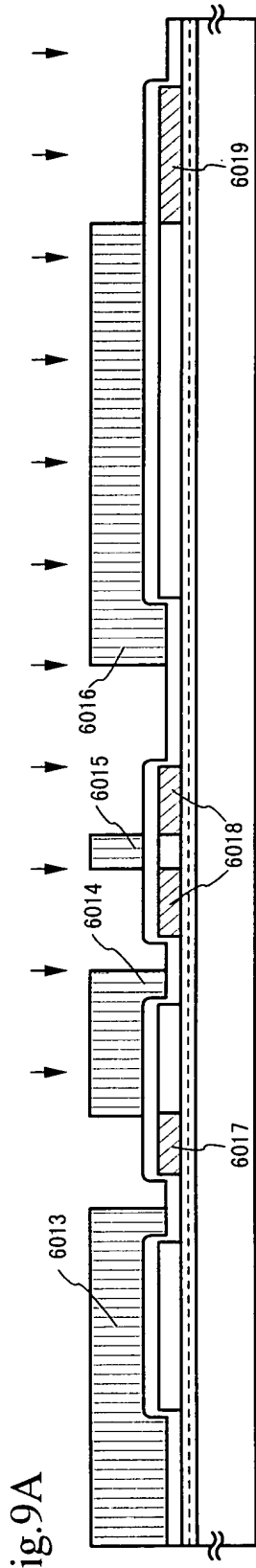


Fig. 9B

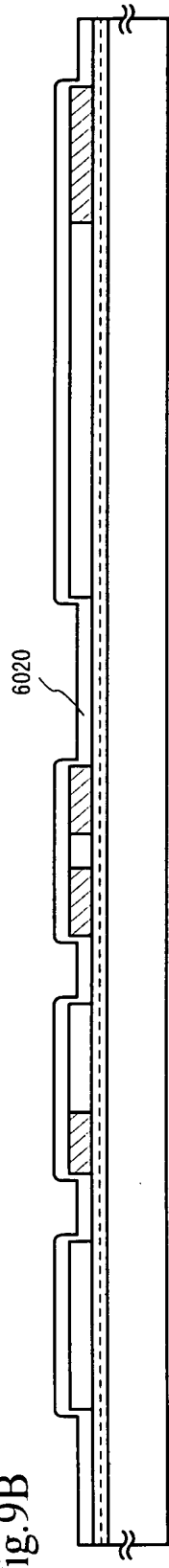


Fig. 9C

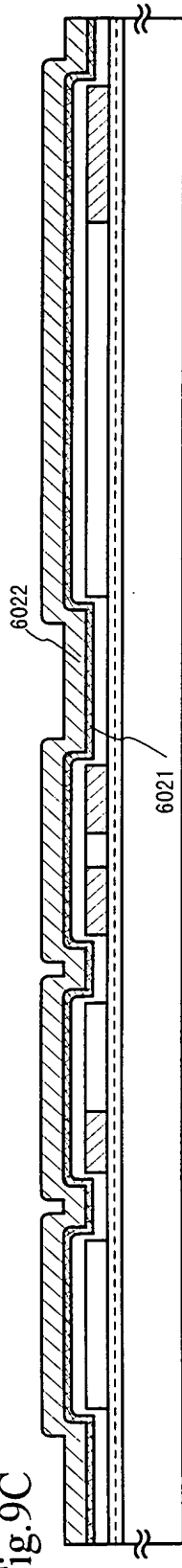


Fig. 9D

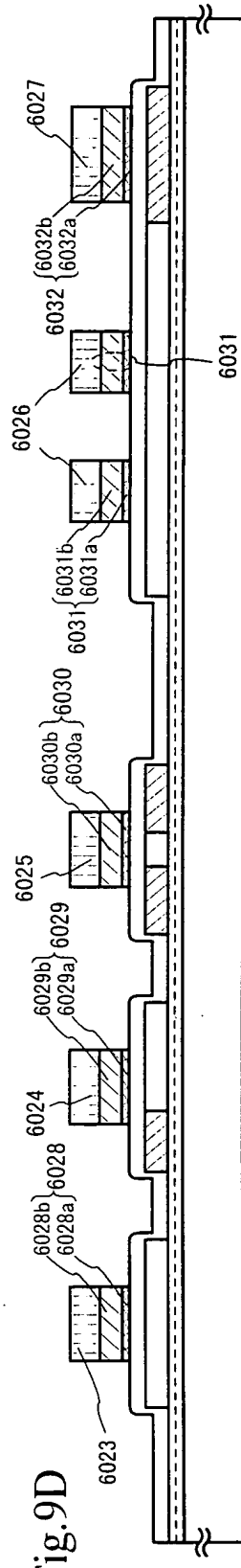


FIG. 10A

Fig.10A

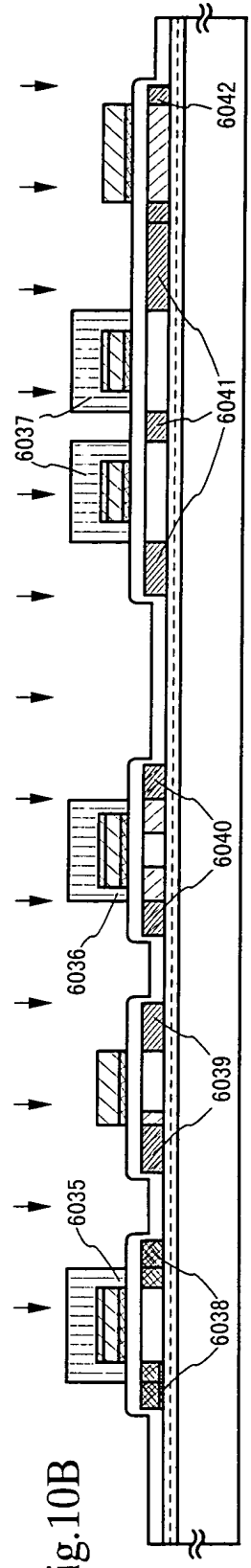
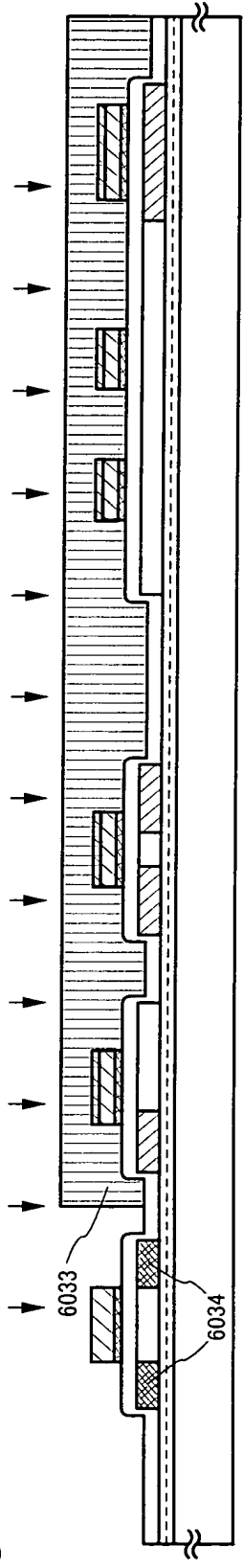


Fig.10B

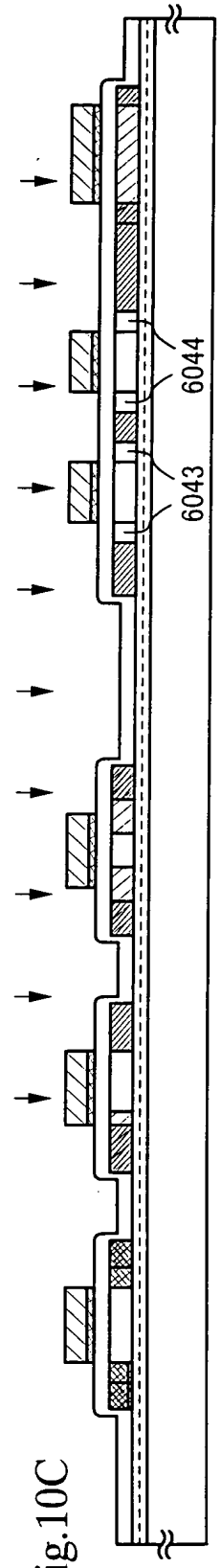


Fig.10C

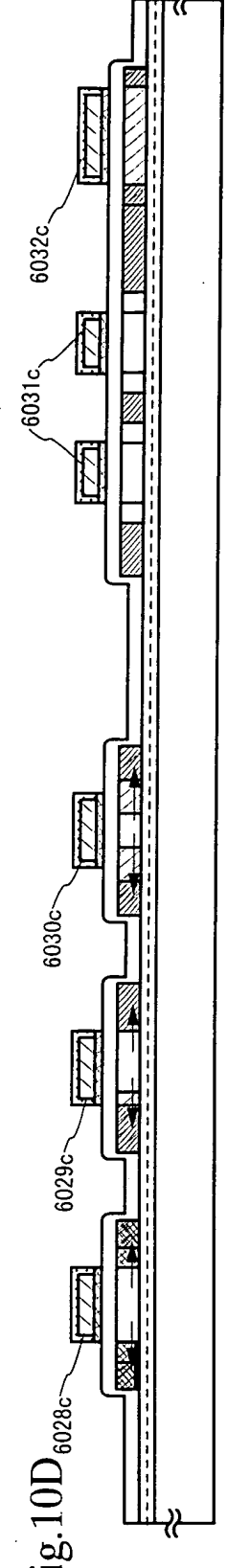


Fig.10D

Fig.11A

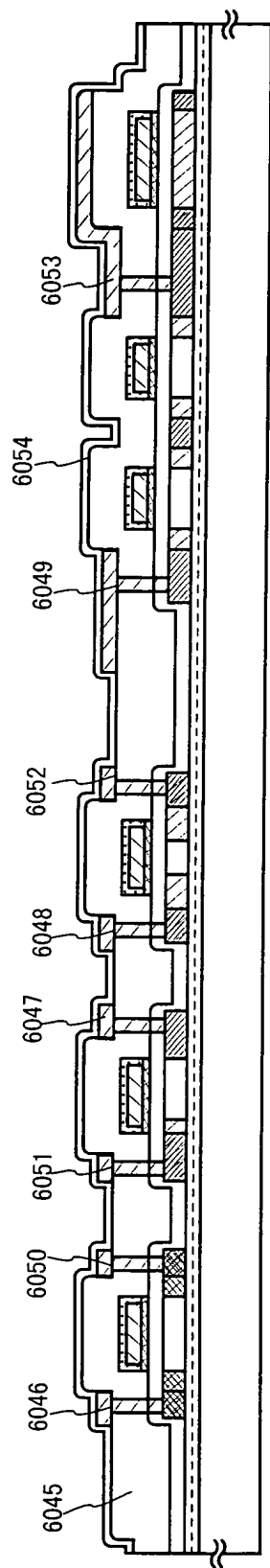


Fig.11B

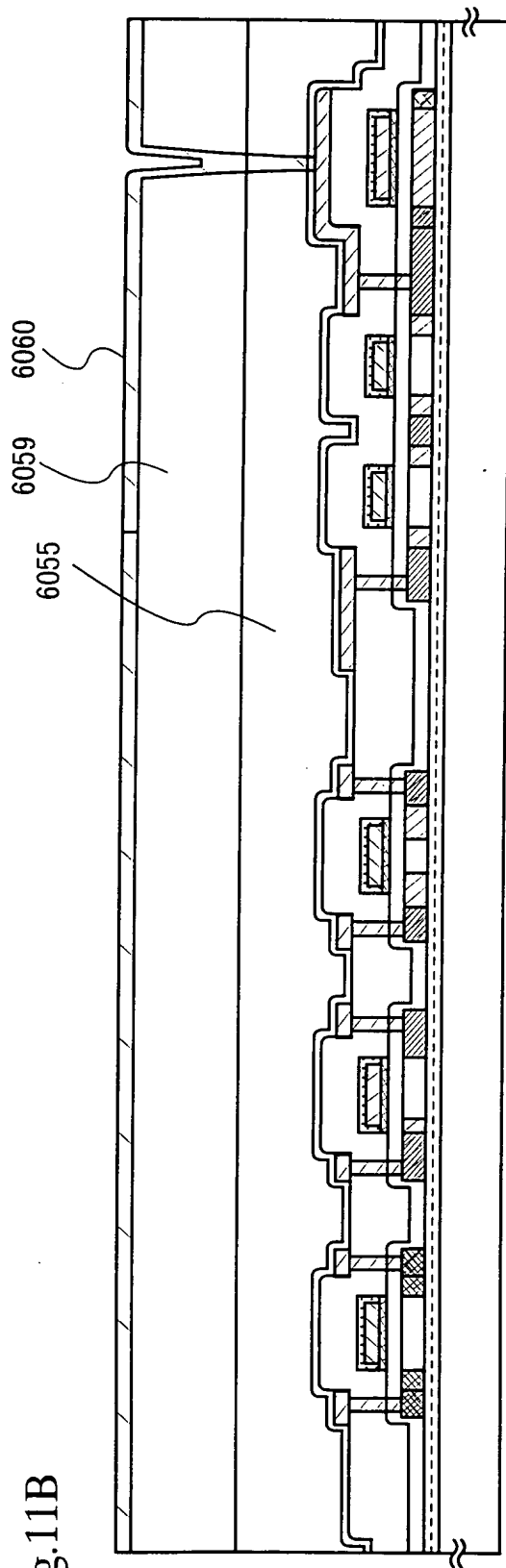


Fig.12

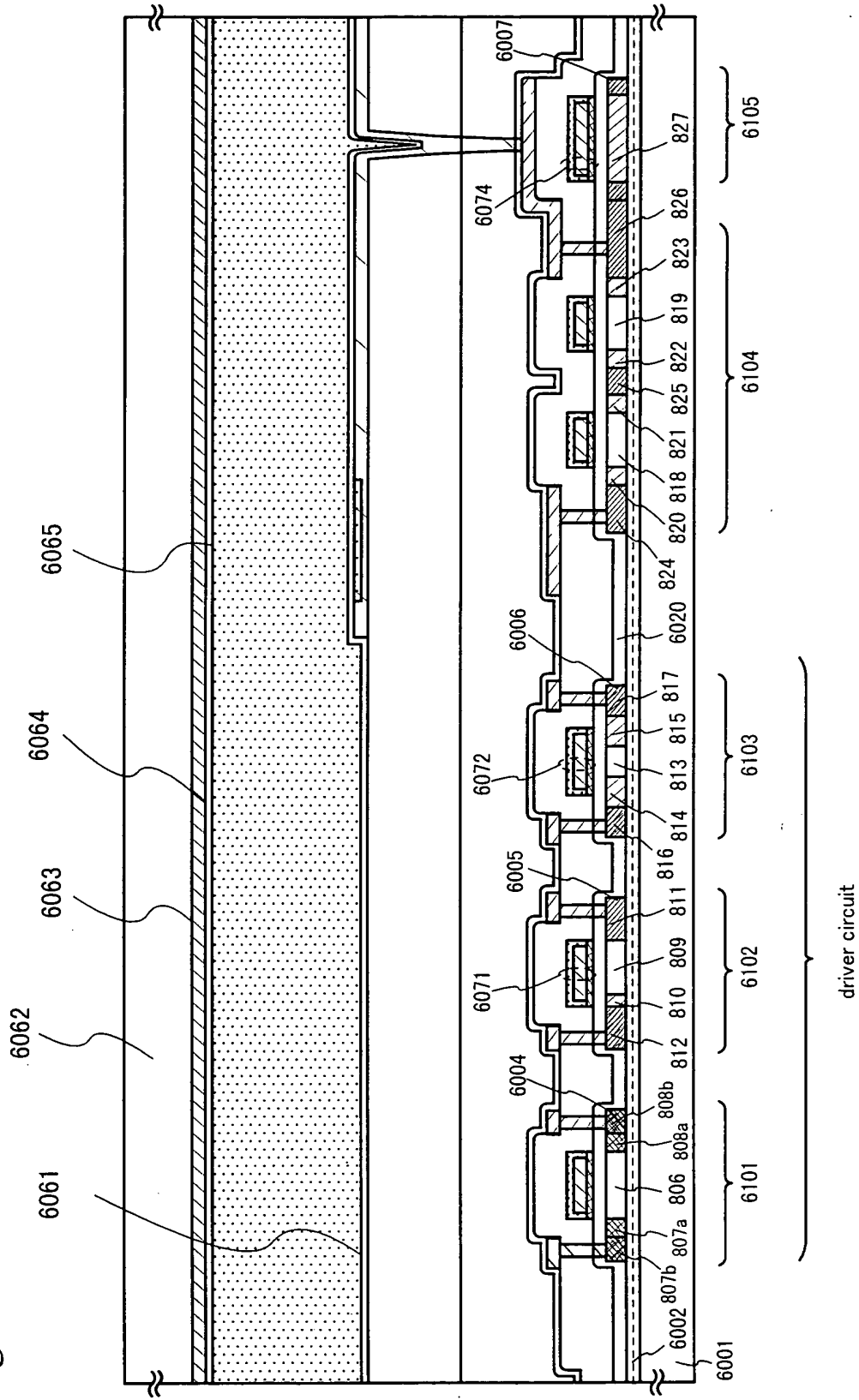
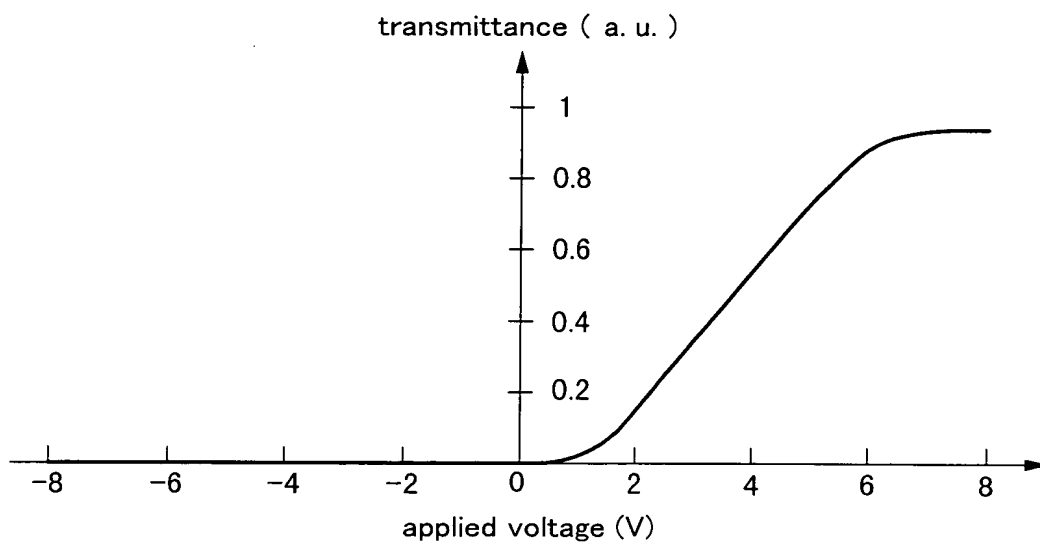


Fig.13



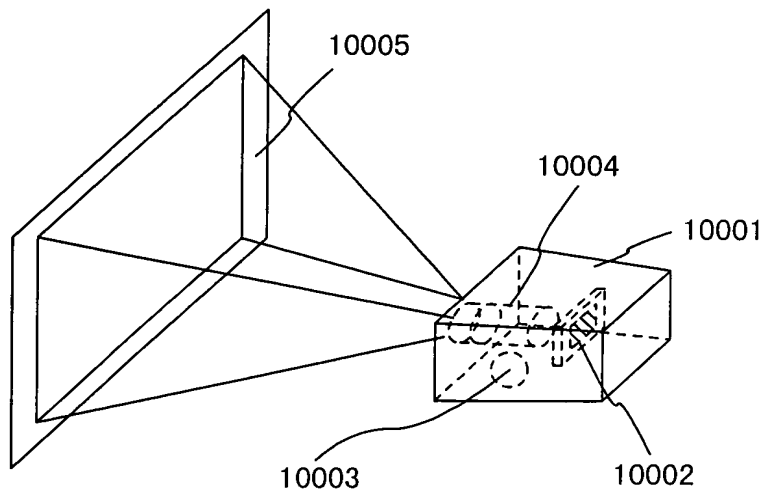


Fig.14A

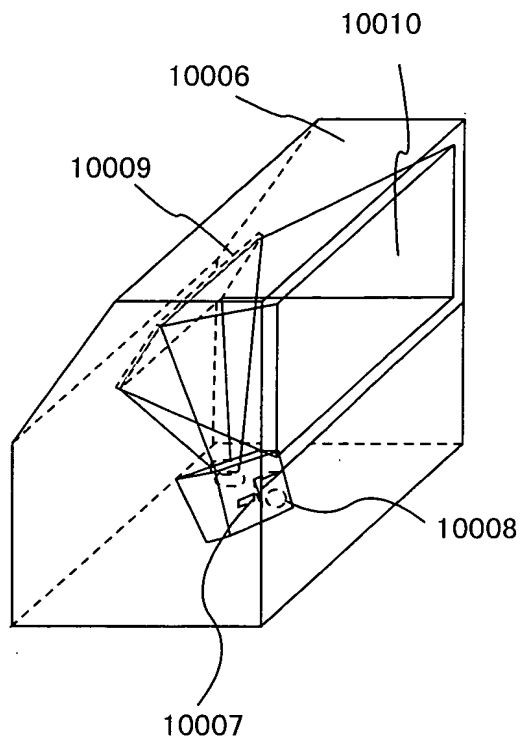


Fig.14B

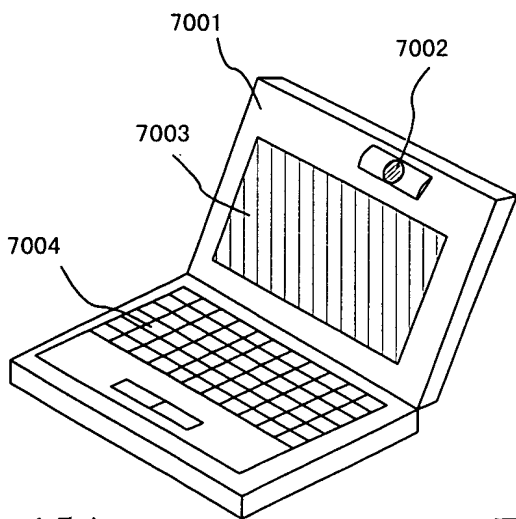


Fig.15A

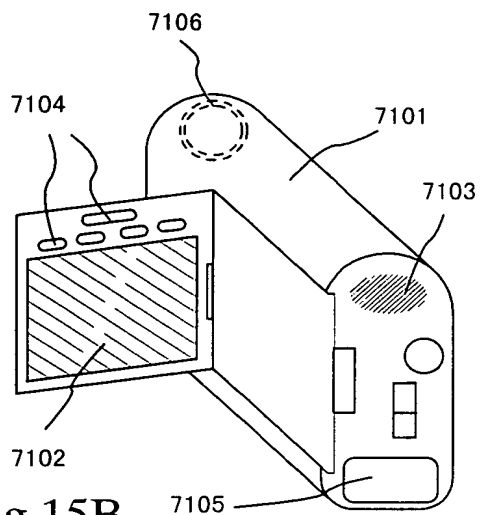


Fig.15B

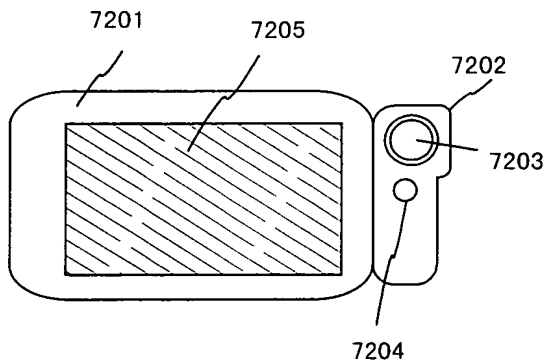


Fig.15C

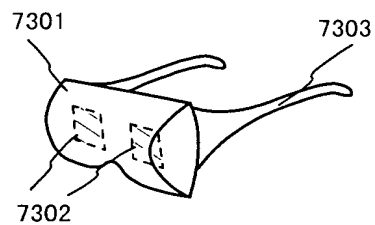


Fig.15D

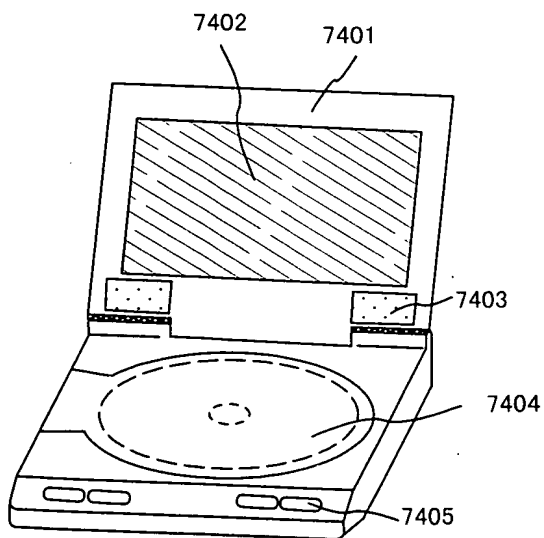


Fig.15E

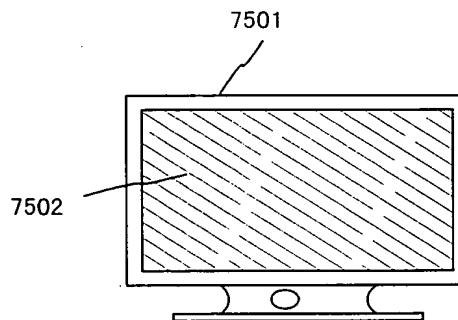


Fig.15F

UDC 621.372.6.01:621.372.6.02:621.372.6.03:621.372.6.04:621.372.6.05:621.372.6.06:621.372.6.07:621.372.6.08:621.372.6.09:621.372.6.10:621.372.6.11:621.372.6.12:621.372.6.13:621.372.6.14:621.372.6.15:621.372.6.16:621.372.6.17:621.372.6.18:621.372.6.19:621.372.6.20:621.372.6.21:621.372.6.22:621.372.6.23:621.372.6.24:621.372.6.25:621.372.6.26:621.372.6.27:621.372.6.28:621.372.6.29:621.372.6.30:621.372.6.31:621.372.6.32:621.372.6.33:621.372.6.34:621.372.6.35:621.372.6.36:621.372.6.37:621.372.6.38:621.372.6.39:621.372.6.40:621.372.6.41:621.372.6.42:621.372.6.43:621.372.6.44:621.372.6.45:621.372.6.46:621.372.6.47:621.372.6.48:621.372.6.49:621.372.6.50:621.372.6.51:621.372.6.52:621.372.6.53:621.372.6.54:621.372.6.55:621.372.6.56:621.372.6.57:621.372.6.58:621.372.6.59:621.372.6.60:621.372.6.61:621.372.6.62:621.372.6.63:621.372.6.64:621.372.6.65:621.372.6.66:621.372.6.67:621.372.6.68:621.372.6.69:621.372.6.70:621.372.6.71:621.372.6.72:621.372.6.73:621.372.6.74:621.372.6.75:621.372.6.76:621.372.6.77:621.372.6.78:621.372.6.79:621.372.6.80:621.372.6.81:621.372.6.82:621.372.6.83:621.372.6.84:621.372.6.85:621.372.6.86:621.372.6.87:621.372.6.88:621.372.6.89:621.372.6.90:621.372.6.91:621.372.6.92:621.372.6.93:621.372.6.94:621.372.6.95:621.372.6.96:621.372.6.97:621.372.6.98:621.372.6.99:621.372.6.100

Fig.16

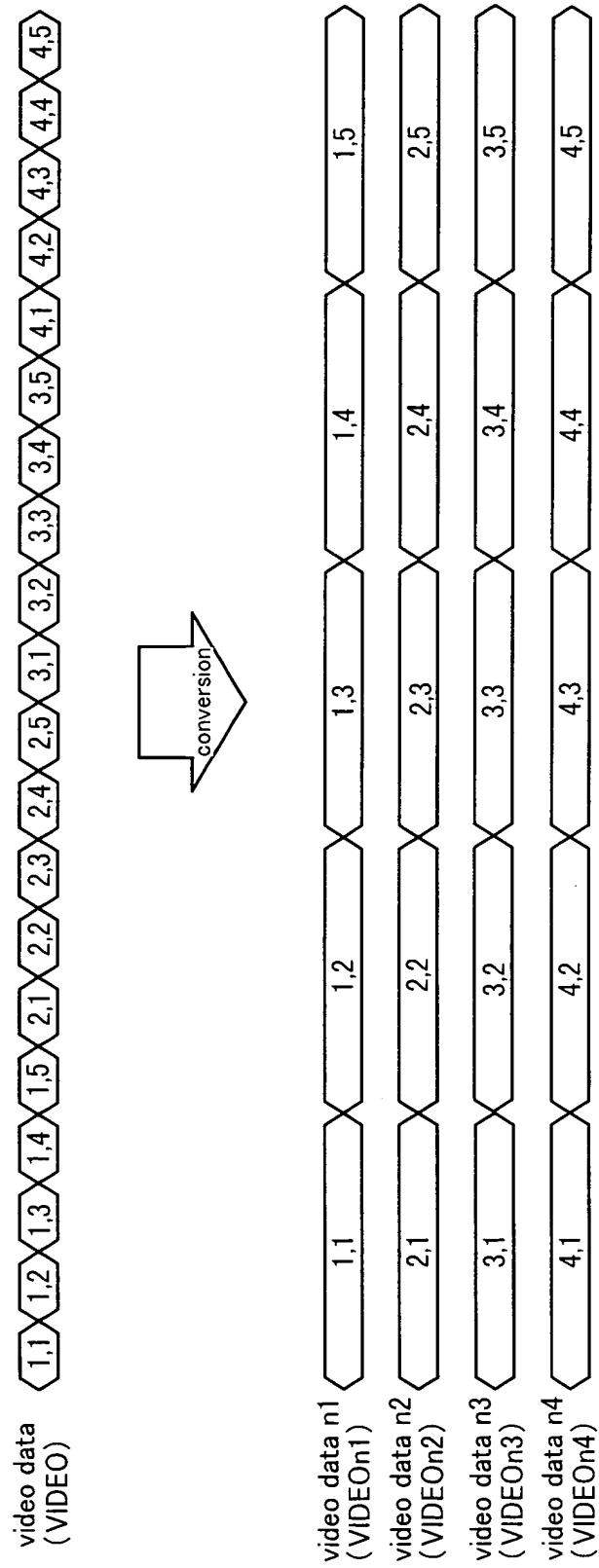




Fig.17

